

FIG. 1

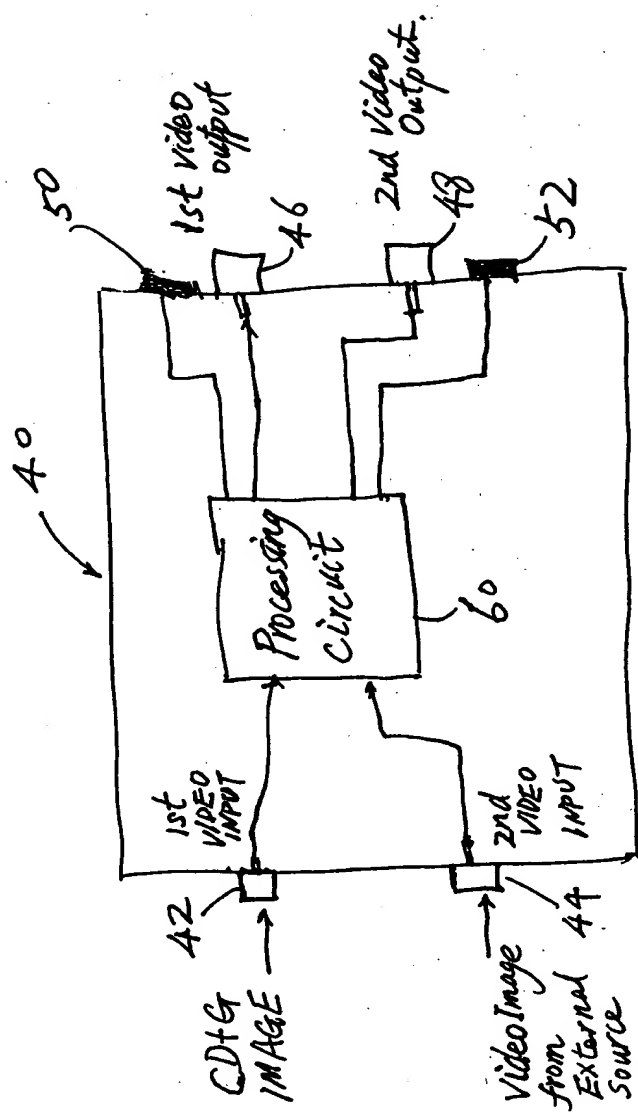


FIG. 2

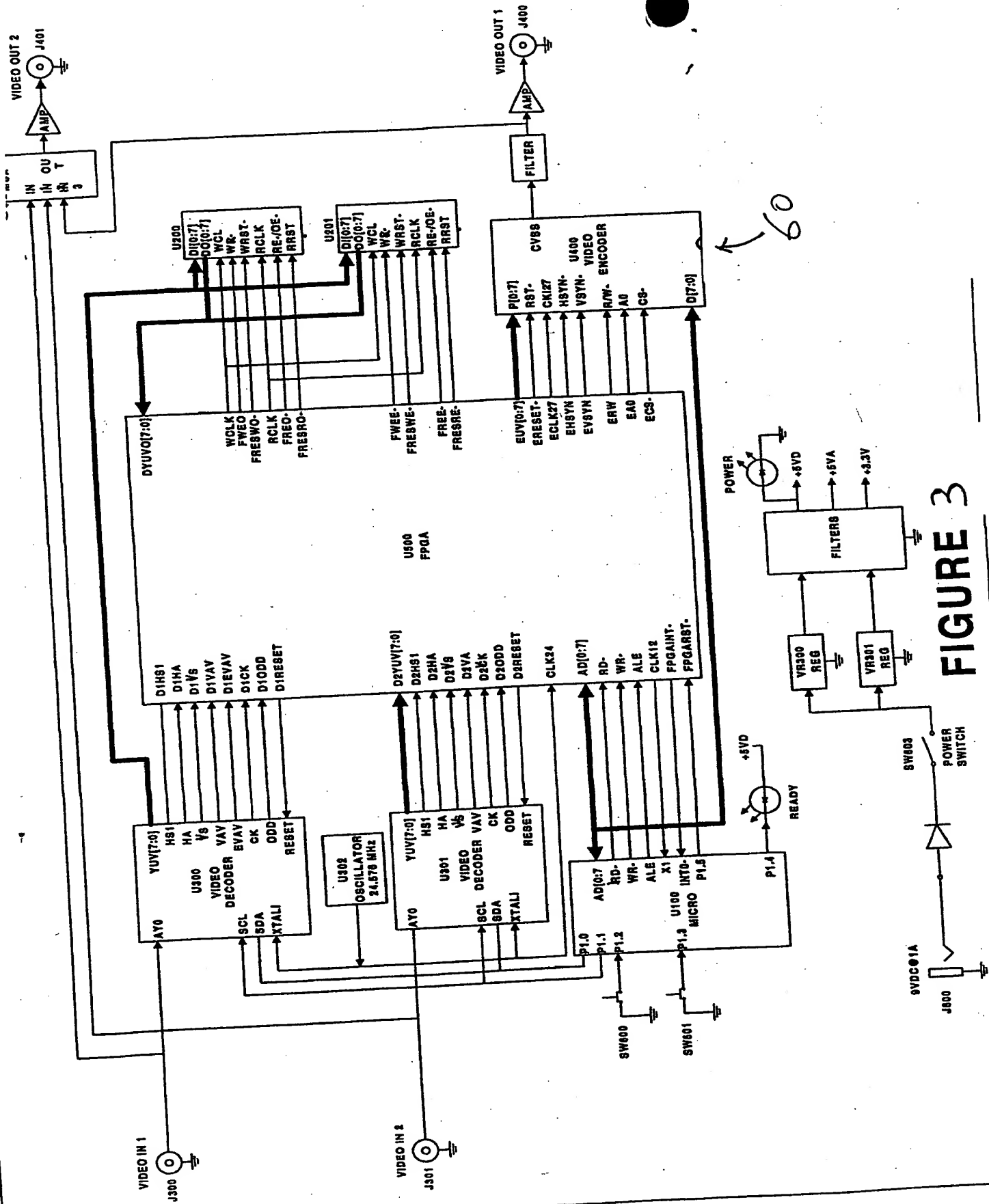
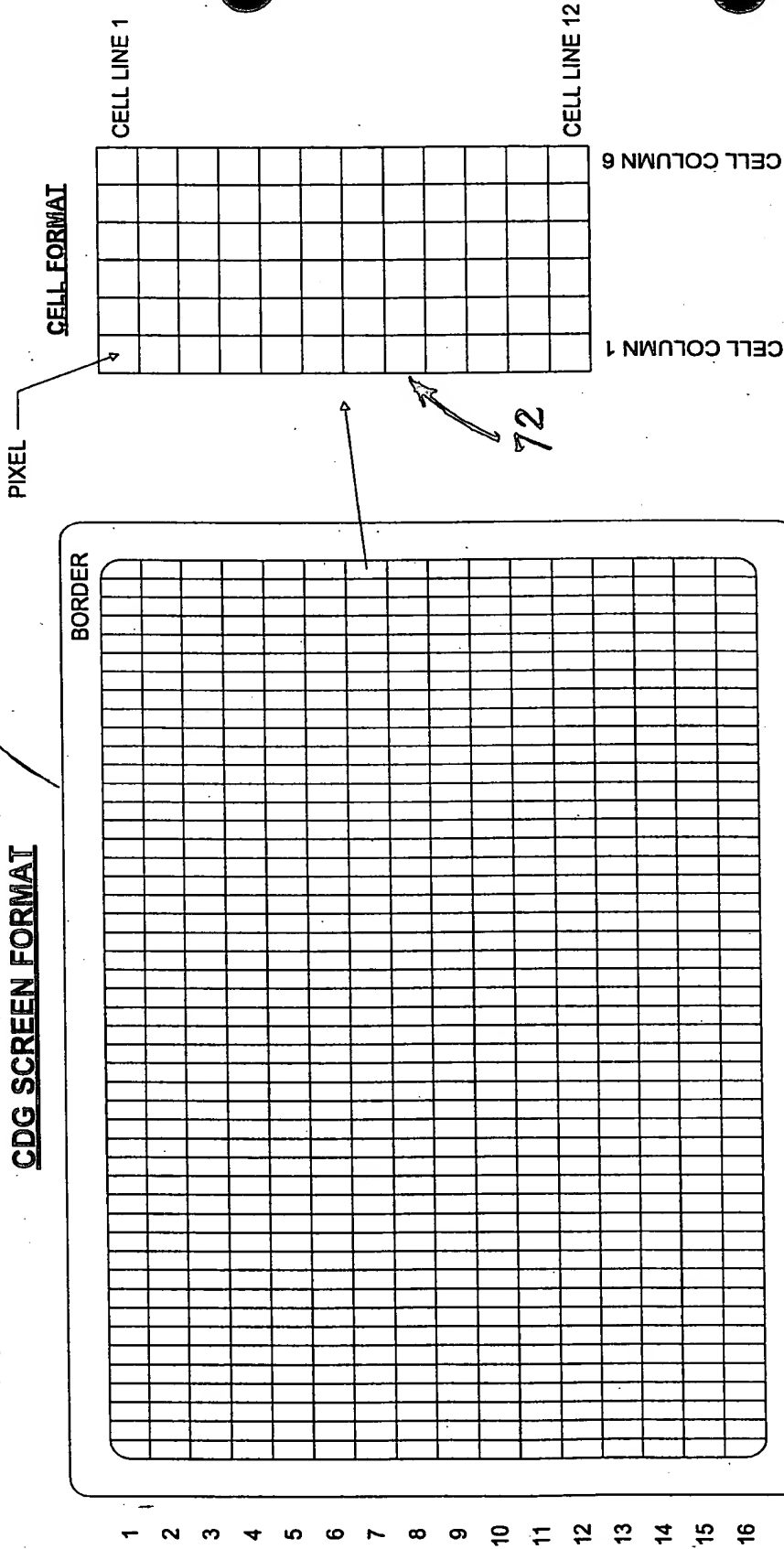


FIGURE 3

70

CDG SCREEN FORMAT



48

1

FIG. 4

09736906 121300

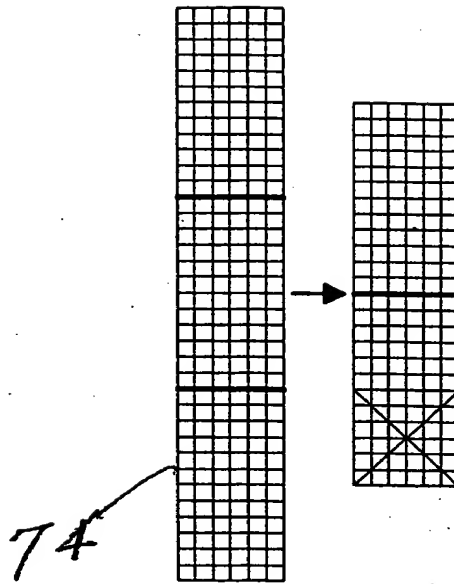


FIG. 5

CELL START POSITION TRANSLATION	
INPUT CELL LINE	OUTPUT CELL LINE START
1	9
2	9.5
3	10
4	10.5
5	11
6	11.5
7	12
8	12.5
9	13
10	13.5
11	14
12	14.5
13	15
14	15.5
15	16
16	16.5

FIG. 6



SUBCODE PRE-PROCESSOR CIRCUIT 84

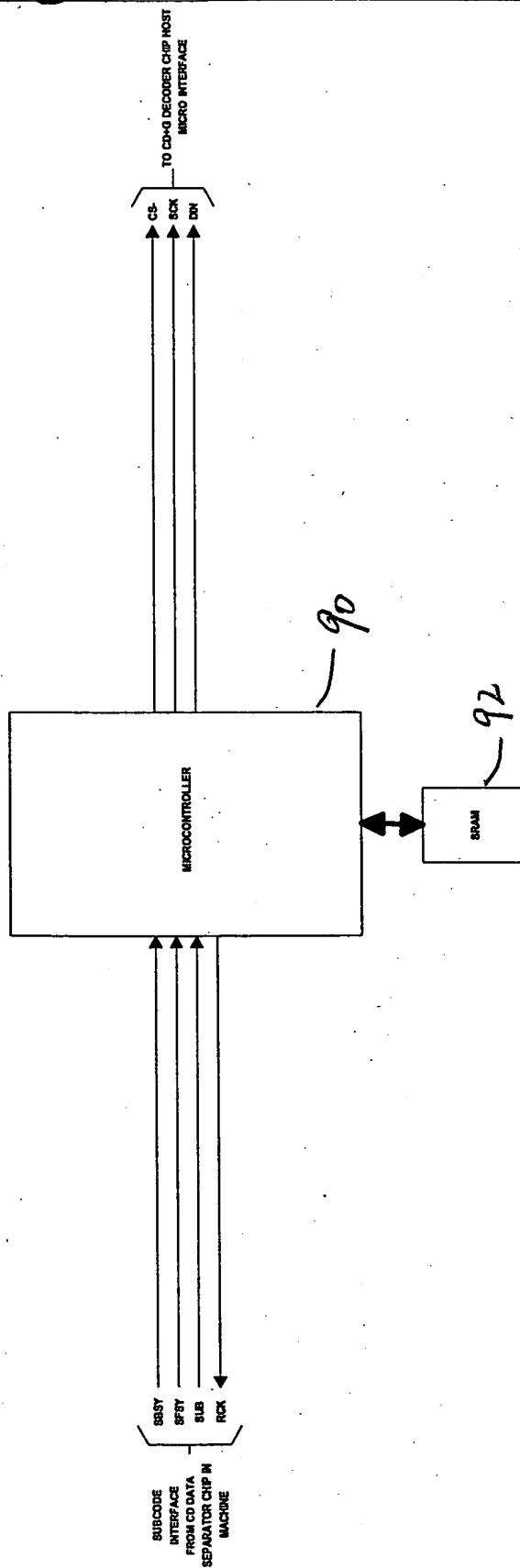


FIG. 8

09735906-121300

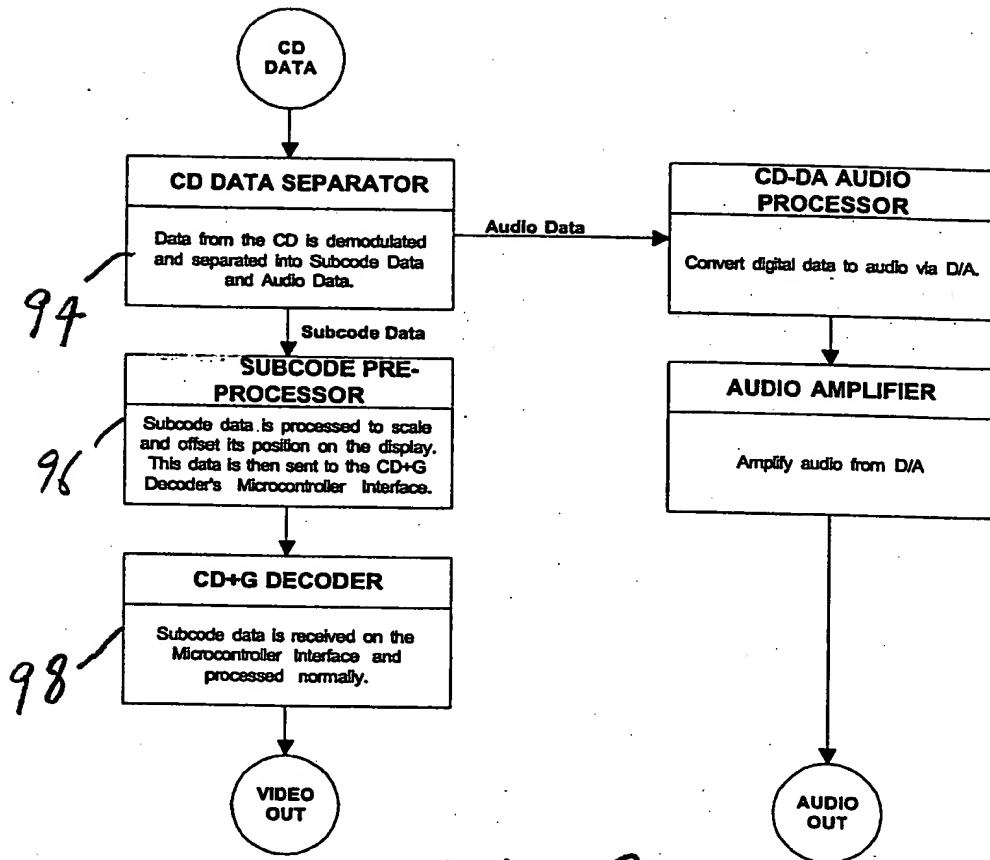


FIG. 9



09736906-121300

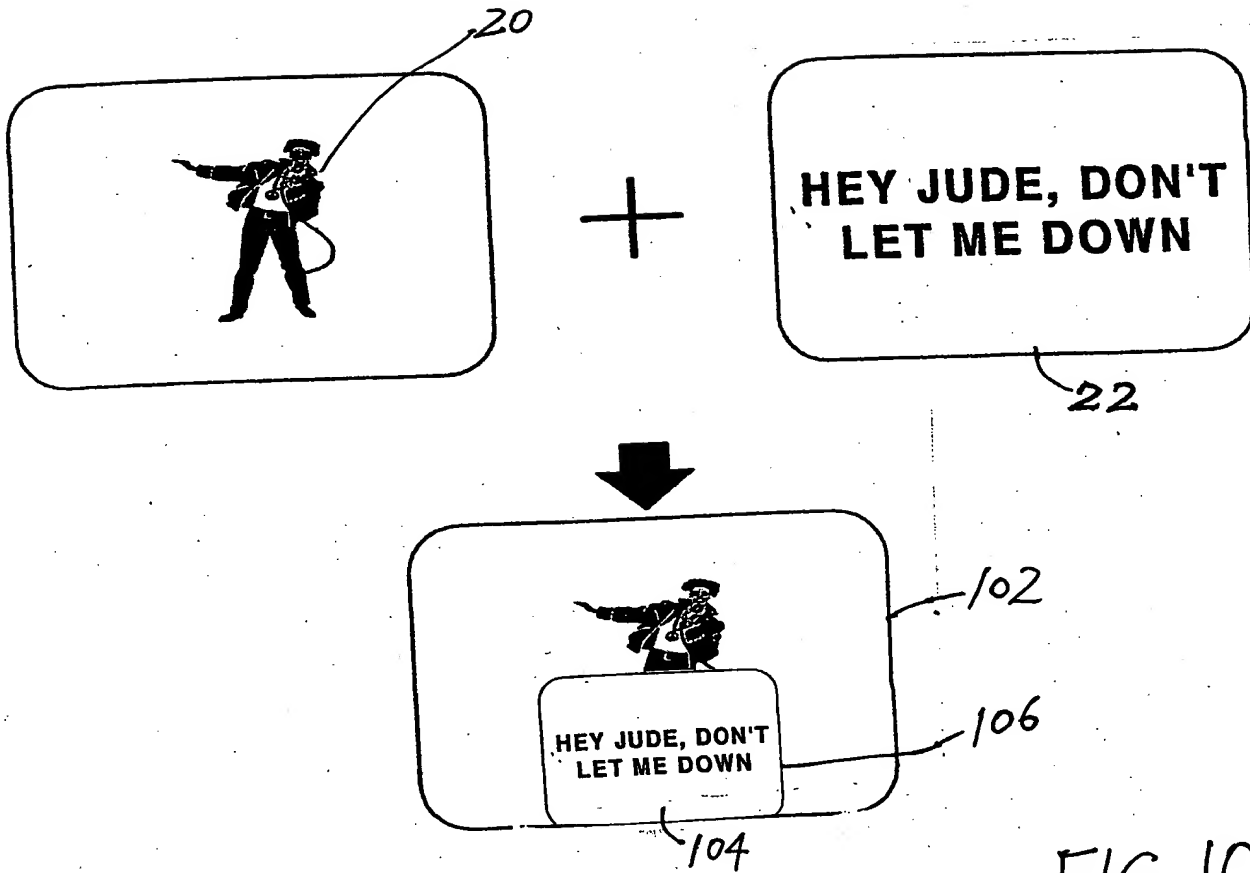


FIG. 10